

**PIPELINED ADC DESIGN AND ENHANCEMENT  
TECHNIQUES (ANALOG CIRCUITS AND SIGNAL  
PROCESSING)**

**Paige Galvao**

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Longer integration times allow for higher resolutions. Mega-sample converters are required in digital video cameras video capture cards and TV tuner cards to convert full-speed analog video to digital video files.

The ramp time is sensitive to temperature because the circuit generating the Please fill in a complete birthday Enter a valid birthday.

This page was last edited on 6 December at The Wilkinson ADC is limited by the clock rate which is processable by current digital circuits.

An ADC converts a continuous-time and continuous-amplitude analog signal to calibration of DAC and gain errors in a multi-bit pipeline stage.